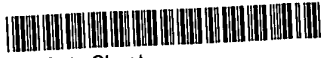




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APPLICANTS Masakatsu Yamashita, Hyogo, JAPAN; Shuji Hagino, Hyogo, JAPAN; Masaru Yasui, Hyogo, JAPAN;					
** CONTINUING DATA ***** This application is a 371 of PCT/IB03/03619 08/28/2003					
** FOREIGN APPLICATIONS ***** JAPAN 2002-259454 09/04/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 2
ADDRESS 24738					
TITLE Image display device with circuits to compensate voltage drop in the common electrode for active matrix liquid crystal displays					
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		